

GENERAL SLOT PATTERN FOR III SEMESTER B. TECH. (ELECTRICAL ENGINEERING)

Day	8:30 – 9:20	9:30 – 10:20	10:30 – 11:20	11:30 – 12:20	Lunch break	14:00 – 14:50	15.00 – 15:50	16.00 – 16:50	17.00 - 17:50
Monday	1A	2A	3A			7A			
Tuesday	5A		7B	9B		6A			
Wednesday		1B	2B	3B					
Thursday		7C	9C	5B			6B		
Friday	3C		1C	2C			5C	6C	

SLOT 1	1A, 1B, 1C	50 Min	SLOT 9	9A, 9B, 9C	50 Min
SLOT 2	2A, 2B, 2C	50 Min	SLOT 10	10A, 10B, 10C	50 Min
SLOT 3	3A, 3B, 3C	50 Min	SLOT 11	11A, 11B, 11C	50 Min
SLOT 4	4A, 4B, 4C	50 Min	SLOT 12	12A, 12B, 12C	50 Min
SLOT 5	5A, 5B, 5C	50 Min	SLOT 13	13A, 13B, 13C	50 Min
SLOT 6	6A, 6B, 6C	50 Min	SLOT 8	8A, 8B, 8C	50 Min
SLOT 7	7A, 7B, 7C	50 Min	SLOT L	L1,L2,L3,L4	2:50 Min

TIMETABLE FOR AUTUMN SEMESTER 2023-2024
III SEMESTER B.TECH. (ELECTRICAL ENGINEERING)

S.No	Course code	Course name	L-T-P-C	Lecture		Instructor	
				Slot	Venue		
1	EE 229	Electronic Devices (1 st Half)	3-0-0-3	1	UG1	Prof. Ruma	
2	EE 202	Introduction to Analog Circuits (2 nd Half)	3-0-0-3	1	UG1	Prof. Nagaveni	
3	EE 205	Network Theory	3-0-0-6	2	UG1	Prof. Vigneshwara Raja	
4	MA 201	Complex Analysis (1 st Half)	3-1-0-4	3A, 9B, 3B, 9C	UG1	Prof. Shreedevi Masuti	
5	MA 203	Differential Equations II (2 nd Half)	3-1-0-4	3A, 9B, 3B, 9C	UG1	Prof. Amlan K. Barua	
6	EE 221	Introduction to Probability (1 st Half)	3-0-0-3	5	D1	UG2	Prof. Rahul Pandya
					D2	UG3	Prof. Bharath B.N.
7	EE 227	Data Analysis (2 nd Half)	3-0-0-3	5	UG3	Prof. Rahul Pandya	
8	HS 201	Economics	2-1-0-6	6	F300	Prof. Gopal Sharan Parashari Prof. Mohana Rao Balaga	
9	EE 210	Signals & Systems	2-1-0-6	7	UG1	Prof. Ameer Mulla	
Total Prescribed Credits for III semester			38	D1 – Division 1, D2 – Division 2 B1- Batch 1, B2- Batch 2			